

November 2009



# THICK-FILM CHIP RESISTOR



**SAMSUNG  
ELECTRO-MECHANICS**





## We, Samsung, declare that our component Chip Resistor is produced in accordance with EU RoHS directive.

### 1. RoHS Compliance and restriction of Br

The following restricted materials are not used in packaging materials as well as products in compliance with the law and restriction.

- Cd, Pb, Hg, Cr+6, As, Br and the compounds, PCB, asbestos
- Bromic materials : PBBs, PBBOs, PBDO, PBDE, PBB

### 2. No use of materials breaking Ozone layer

The following ODS materials are not used in our fabrication process.

- ODS material : Freon, Haron, 1-1-1 TCE, CCl4, HCFC

If you want more detailed Information, Please Visit Samsung Electro-mechanics Website <http://www.sem.samsung.com>



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# Operation Notes

## Applications

- Chip resistors are designed for general electronic devices such as home appliances, computer, mobile communications, digital circuit, etc. If you require our products with high reliability-performing at more than 125°C or below -55°C- for medical equipments, aircraft, high speed machines, military usage, and items that can affect human life or if you need to use in specific conditions (corrosive gas atmosphere like H<sub>2</sub>S etc.), please contact us beforehand.
- Normal operation temperature ranges (°C) as follows.  
-1608, 2012, 3216(general, precision) : -55°C~+155°C  
-Others (rectangular, array, trimmable) : -55°C~+125°C
- Although resistor body is coated, sharp excessive impact should be avoided to prevent damages and adverse effects on characteristics (resistor value, open circuited, T.C.R.).

## Mounting

Please give more attention not to press the chip owing to the nozzle's improper height when it is mounted on PCB.  
(Excessive pressure may cause exterior damage, change in resistance, circuit open, etc.)

## Soldering

Our products have Ag electrodes protected by double layer.

- 1st Ni coating  
- This prevents Ag electrode from leaching and enhances the bonding with Sn.
- 2nd Sn coating  
- This is made of Sn 100% with melting point 232°C to prevent it from melting when solder cream melts, and to enhance the bonding. Commercial solder creams are made of Sn-3.0Ag-0.5Cu with melting point 217°C.

## Safety precautions

This products are designed and produced for application in ordinary electronic equipment (AV equipment, OA equipment, telecommunication equipment, ... etc)

if the products are to be used in devices requiring extremely high reliability (Medical equipment, transport equipment, aircraft / spacocraft, nuclear power controllers, fuel controllers, car equipment including car accessor, safety device, etc)

- a) Installation of protection circuits or other protective devices to improve system safety.
- b) Installation of redundant circuits or other productive devices to improve system safety.

## Storage

To maintain proper quality of chip components, the following precautions are required for storage environment, method and period.

- Storage Environment
  - Make sure that the ambient temperature is within 5°C~40°C and the ambient humidity is within 20~70%RH.
  - Chip components may be deformed, if the temperature of packaged components exceeds 40°C.
  - Do not store where the soldering properties can be deteriorated by harmful gas such as sulphurous gas, chlorine gas, etc.
  - Bulk packed chip components should be used as soon as the seal is opened, thus preventing the solderability from deteriorating.
  - The remaining unused chips should be put in the original bag and sealed again or store in a desiccator containing a desiccating agent.
- Storage Time Period  
Stored chip components should be used within 6 months after receiving the components. If 6 months or more have elapsed, please check the solderability before actually using.

## Cleaning

After Soldering Cleaning, soldering flux & Ionic cleaning liquid should be avoided on product.

If any possibility on product, please take a test before usage.

## Caution for Chip Resistor Separation from PCB.

Chip resistor installation on PCB is similar phenomenon on chocolate chip on top of cake.

PCB has enough flexibility on outer force but Chip resistor can be defected without any bending.

(By chip resistor use of Ceramic, solder, metal)

Therefore, when separate from Chip resistor on PCB, be ware of any crack of chip.

## Others

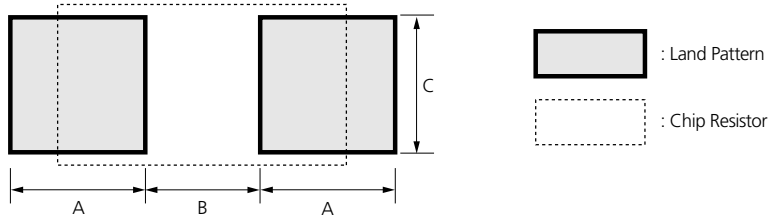
- Manual work  
Whenever separating chip resistor from PCB, do not re-use the chip resistor for circuit safety.  
Chip resistor can be electrical specification change by soldering Iron after separation.  
Re-use of separated chip resistor should be prohibited.
- Do not use more than rated voltage.  
(Please check the contents of each product)

# Example of Land Pattern Design

## Abstract

- When designing P.C.B, the shape and size of the solder lands must allow for the proper amount of solder under the resistor. The amount of solder at the end terminations has a direct effect on the probability that the chip will crack. The greater amount of solder, the amount of stress on the chip, and the more likely that it will break. Use the following illustrations as guidelines for proper 'solder lands design'.

## For Rectangular Type



• Flow soldering (UNIT: mm)

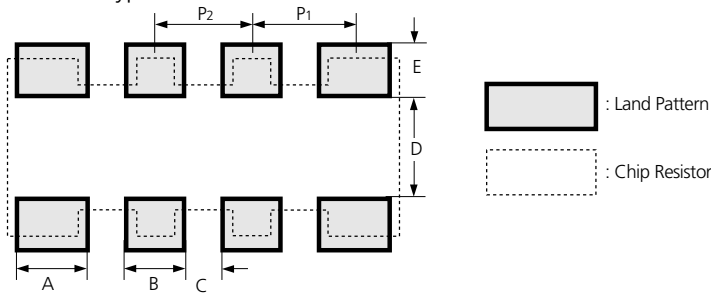
Type	A	B	2A+B	C
1005	0.7	0.5	1.9	0.5
1608	0.9	0.8	2.6	0.8
2012	1.0	1.4	3.4	1.3
3216	1.4	1.8	4.6	1.6
3225	1.4	1.8	4.6	2.6
5025	1.5	3.3	6.3	2.5
6432	1.5	4.6	7.6	3.2

• Reflow soldering (UNIT: mm)

Type	A	B	2A+B	C
0603	0.37	0.28	1.02	0.29
1005	0.6	0.5	1.7	0.5
1608	0.8	0.8	2.4	0.8
2012	0.9	1.4	3.2	1.2
3216	1.3	1.8	4.4	1.5
3225	1.3	1.8	4.4	2.4
5025	1.4	3.3	6.1	2.4
6432	1.4	4.6	7.4	3.0

## For Array Type

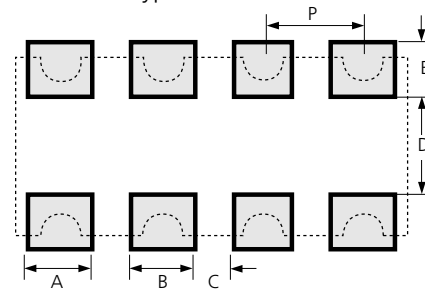
• Convex type



(UNIT: mm)

Type	A	B	C	D	E	P1	P2
10AT	0.4	0.4	0.25	0.5	0.5	0.65	-
102P	0.4	0.4	0.25	0.5	0.5	0.65	-
104P	0.4	0.3	0.2	0.5	0.5	0.55	0.5
164P	0.7	0.5	0.3	0.9	0.8	0.9	0.8

• Concave type



(UNIT: mm)

Type	A	B	C	D	E	P
102P	0.3	0.3	0.2	0.5	0.4	0.5
104P	0.3	0.3	0.2	0.5	0.4	0.5
162P	0.5	0.3	0.4	0.8	0.8	0.8
164P	0.5	0.3	0.4	0.8	0.8	0.8

- Please refer to this land pattern when you design P.C.B. if you want. This pattern can't guarantee any characteristic of your product.

Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

Array

Attenuator

Characteristics Performance

Packaging

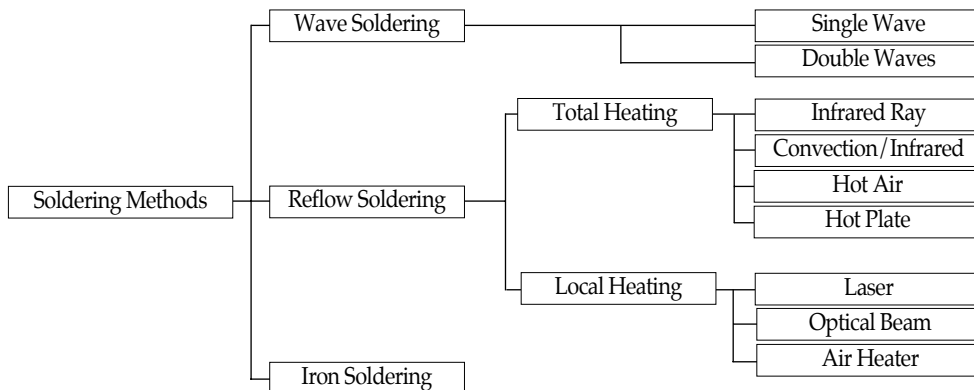
Standard Resistance Value

# S A M S U N G E L E C T R O M E C H A N I C S

## Recommended Soldering Conditions

### Abstract

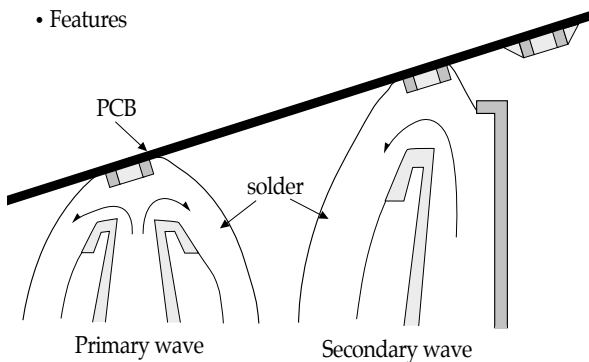
- There are 3 soldering methods.
  - Flow(wave) soldering.
  - Reflow soldering. (Reflow soldering is broadly divided into the total heating method and local heating method.)
  - Iron soldering.



Since Chip resistors come into direct contact with melted solder during soldering, it is exposed to potentially mechanical stress caused by the sudden temperature change. The chip resistors may also be subject to silver migration, and to contamination by the flux. Because of these factors, soldering technique is critical.

### Flow(wave) Soldering

- Features



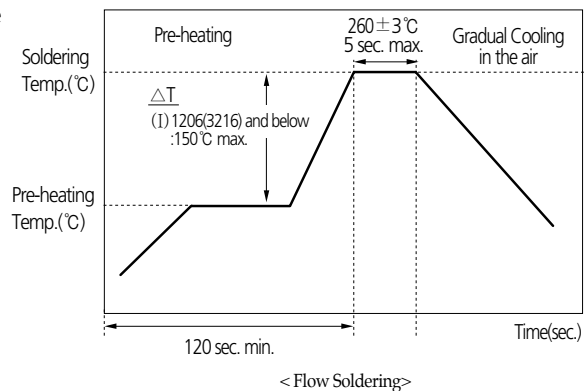
There are two types of soldering methods in flow(wave) soldering. One is single wave soldering, the other is a double wave soldering. However, double waves soldering is mainly used. This method is designed so that the continuous and multiple dipping processes by waves of solder having completely different primary and secondary characteristics and waveforms. With the primary wave, a comparatively strong jet flow is used to remove the flux gas and to solder. With the secondary wave, it is used to remove excessive solder. With the primary wave, the solder flows into a very small gap between components and air bubbles remaining on the soldered joint are removed. With the secondary wave, the peel back is used to prevent bridging.

- Preheating

If a chip component is heated suddenly during soldering, it may be cracked by the thermal shock caused by the temperature difference between the surface and the inside of the chip. To prevent this, a full preheating is necessary. In case of wave soldering, the temperature difference between solder and surface of the component is kept within 150°C. Also when cooling is done by dipping into solvent, care should be taken to keep the temperature difference within 150°C.

- Standard Soldering Condition

Soldering must be carried out without exceeding the approved soldering temperature and time shown within the shaded area of the right graph. An excessively long soldering time or high soldering temperature results in leaching of the outer terminations. When a PCB is warped, mechanical stress applied to the chip will be increased and might be a cause of chip crack, especially if there is big amount of solder on the chip. So, care should be taken not to use excessive amount of solder on the PCB. For the flow(wave) soldering, the solder amount can be controlled by land size.



## Reflow Soldering

### • Pre-heating and cooling

In the reflow soldering method, a full pre-heating at the proper temperature is necessary to dry and activate solder paste. Tomb-stoning can be reduced by preheating at 150~180°C for more than 1 minute. Also when cooling is done by dipping into solvent, care should be taken to keep the temperature difference within 150°C.

### • Standard Reflow Soldering Condition

Soldering must be carried out without exceeding the approved soldering temperature and time shown within the shaded area of the right graph. This prevents the terminations from leaching and characteristics from deteriorating. When soldering is repeated, the allowed time is the accumulated time.

### • Standard solder amount

When a PCB is warped, mechanical stress applied to the chip should be reduced, and for doing so, care should be taken not to use excessive amount of solder on the PCB. In case of the reflow method, the thickness of the coated solder paste is controlled to prevent excessive solder. The thickness of solder paste should be 100~300 $\mu$ m.

### • Tombstoning and Prevention

When reflow soldering, or especially vapor phase soldering (VPS), small chip components of less than RC3216 type may break away from solder and stand on end. This is commonly known as tombstoning or the Manhattan phenomenon.

#### - Cause of tombstoning

PCB: Form and size of land, component layout and material

Chip component: Form, size, weight and solder wetting properties

Solder paste: Particle form, particle diameter, degree of activation and coated amount

Machine: Pre-heating condition, unbalance heat system and mechanical vibration

#### - Preventing tombstoning

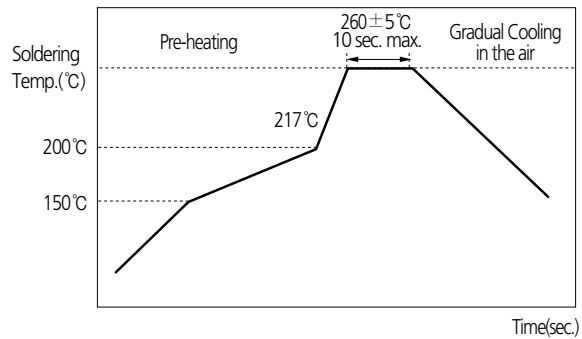
Keep land size as small as possible.

Pre-heat (Pre-heating temperature: 150~180°C, Pre-heating time: More than 1 minute)

Keep the amount of solder paste low.

Keep position deviation at a minimum when mounting chips.

When soldering, reduce thermal unbalance at both electrode of a chip.



< Reflow Soldering >

Operation  
Notes

Example of Land  
Pattern Design

Recommended  
Soldering Conditions

General  
Purpose

Precision

Low Ohms

Array

Attenuator

Characteristics  
Performance

Packaging

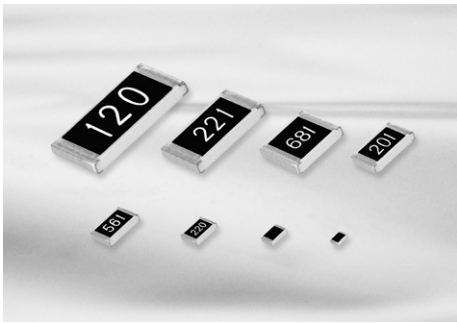
Standard  
Resistance Value

## Iron Soldering

When using a soldering iron or any other soldering operation, the permissible temperature and time should not be exceeded that in the reflow soldering. In other to prevent the external terminations from leaching and characteristics from deteriorating, the tip of the soldering iron should not touch the chip component (ceramic element, resin case, etc.). Soldering with a soldering iron and correcting with a soldering iron can be performed right the following conditions.

Item	Condition
Temperature at tip	350°C Max.
Soldering iron output	20-Watt Max.
End of soldering iron	∅3mm Max.
Note	Do not directly touch the chip by the tip of the iron.

# General Purpose



## Feature

- Very small, thin, and light weight.
- Both flow and reflow soldering are applicable.
- Owing to the reduced lead inductance, the high frequency characteristic is excellent.
- Suitable size and packaging for surface mount assembly.

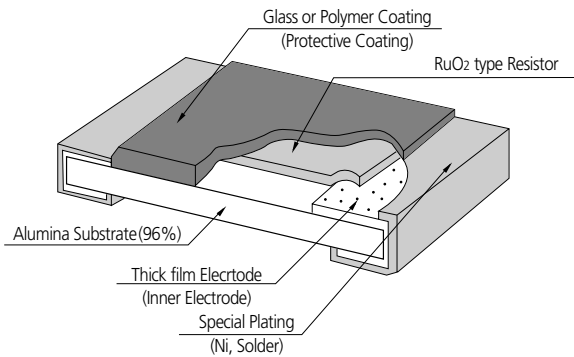
The product of lead-free terminal is RoHS compliant. PbO(lead oxide) is included in the glass of our product which is prescribed on RoHS appendix as an exception.

## Application

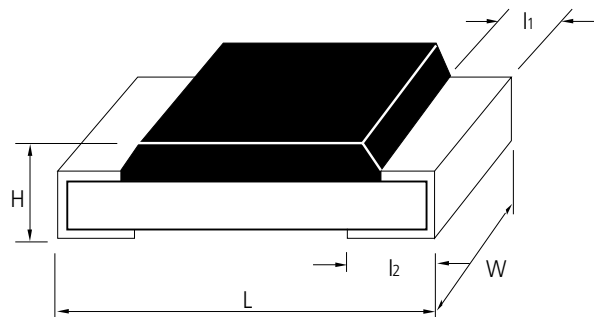
- General purpose
- Home Appliances (DVD, Digital TV, CAMCODER, VTR, Digital Camera, Audio, Tunner)
- For Computers & Communications (Notebook, Memory Module, Mobile, Network Equipment, etc)

## Structure and Dimensions

### • Structure



### • Dimensions



(UNIT: mm)

Type	Inch	Power(W)	L	W	H	l <sub>1</sub>	l <sub>2</sub>	Average Weight
RC0603	0201	1/20	0.60±0.03	0.30±0.03	0.23±0.03	0.1±0.05	0.15±0.05	0.15mg
RC1005	0402	1/16	1.00±0.05	0.50±0.05	0.35±0.05	0.20±0.10	0.25±0.10	0.6mg
RC1608	0603	1/10	1.60±0.10	0.80±0.15	0.45±0.10	0.30±0.20	0.35±0.10	2.1mg
RC2012	0805	1/8	2.00±0.20	1.25±0.15	0.50±0.10	0.40±0.20	0.35±0.20	4.9mg
RC3216	1206	1/4	3.20±0.20	1.60±0.15	0.55±0.10	0.45±0.20	0.40±0.20	9.5mg
RC3225	1210	1/3	3.20±0.20	2.55±0.20	0.55±0.10	0.45±0.20	0.40±0.20	16mg
RC5025	2010	2/3	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20	26mg
RC6432	2512	1	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20	41mg

## Parts Numbering System

- The part number system shall be in the following format

RC	2012	J	100	CS	
Code Designation	Dimension & Size Code	Tolerance	Resistance Value	Packaging Code	
RC: Chip Resistor	0603: 0.6 × 0.3(mm) - 0201(inch)	G : ±2%	3 or 4 digits coding system (IEC coding system)	GS: Bulk Packaging	
	1005: 1.0 × 0.5(mm) - 0402(inch)	J : ±5%		CS: Tape Packaging 7"	
	1608: 1.6 × 0.8(mm) - 0603(inch)	K : ±10%		3digits (E-24 series)	ES: Tape Packaging 10"
	2012: 2.0 × 1.2(mm) - 0805(inch)			4digits (E-96 series)	FS: Tape Packaging 13"
	3216: 3.2 × 1.6(mm) - 1206(inch)	* Jumper: 'J'		AS: Tape Packaging 13"	
	3225: 3.2 × 2.5(mm) - 1210(inch)				
	5025: 5.0 × 2.5(mm) - 2010(inch)				
	6432: 6.4 × 3.2(mm) - 2512(inch)				

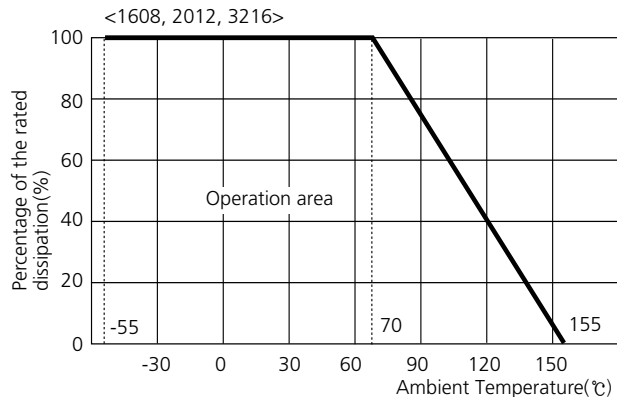
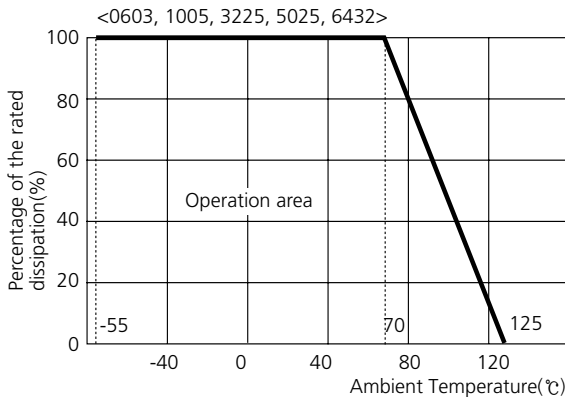
## Specification

Type	Power Rating (W)	Working Voltage (MAX)	Overload Voltage (MAX)	TCR (ppm/°C)	Resistance Range (Ω)			Rated Ambient Temperature	Rated Working Temperature
					G(±2%) E-48	J(±5%) E-24	K(±10%) E-12		
RC 0603	1/20	25(V)	50(V)	1 Ω ≤ R < 10 Ω +300/-200ppm	1 Ω ~ 1M Ω	1 Ω ~ 10M Ω	1 Ω ~ 10M Ω	70 °C	-55 °C ~ +125 °C
RC 1005	1/16	50(V)	100(V)						
RC 1608	1/10	150(V)	300(V)	10 Ω ≤ R ≤ 1M Ω ± 100ppm (0603: ± 250ppm)	1 Ω ~ 1M Ω	1 Ω ~ 10M Ω	1 Ω ~ 10M Ω	70 °C	-55 °C ~ +155 °C
RC 2012	1/8								
RC 3216	1/4	200(V)	400(V)	1M Ω < R ≤ 10M Ω ± 300ppm	1 Ω ~ 1M Ω	1 Ω ~ 10M Ω	1 Ω ~ 10M Ω	70 °C	-55 °C ~ +125 °C
RC 3225	1/3								
RC 5025	2/3								
RC 6432	1								

- Rated voltage (V) =  $\sqrt{\text{Rated power(W)} \times \text{Normal resistance value (R)}}$   
Rated voltage should be lower than (MAX) working voltage.

## Power Derating Curve

The rated power is the maximum continuous loading power at 70 °C ambient temperature.  
For ambient temperature above 70 °C, the loading power follows the below power derating curve.  
(The load current shall be derated according to derating curve in case of the 'Jumper')



## Jumper Resistors

Type	Resistance	Current Rating	Rated Ambient Temperature	Rated Working Temperature
RC 0603	50mΩ Max.	0.5(A)	70 °C	-55 °C ~ +125 °C
RC 1005		1.0(A)		
RC 1608				
RC 2012		2.0(A)		
RC 3216				
RC 3225				
RC 5025				
RC 6432				

## Marking

### 3 digits indication (E-24 series)

- Left 2 digits represent significant figures.
- Last 1 digit represent exponential number of 10.
- Example: **103**  
Left 2 digits: 10  
Last 1 digit: 3  
 $103 = 10 \times 10^3 \Omega$   
 $= 10000 \Omega = 10k\Omega$



- Jumper chip is printed as 000
- Resistance below 10 Ω is expressed using "R"  
ex) 7R5=7.5 Ω
- 0603, 1005 type: No marking.

Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

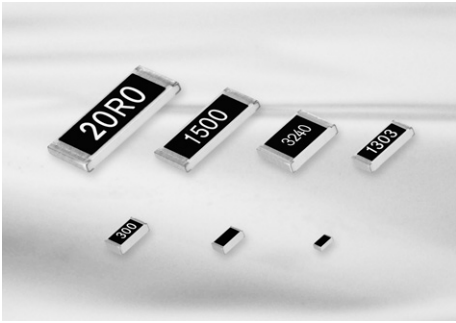
Array

Attenuator

Characteristics Performance

Packaging

Standard Resistance Value



## Feature

- Low tolerance ( $\pm 1\%$ )
- Both flow and reflow soldering are applicable.
- Suitable size and packaging for surface mount assembly.
- Owing to the reduced lead inductance, the high frequency characteristic is excellent.

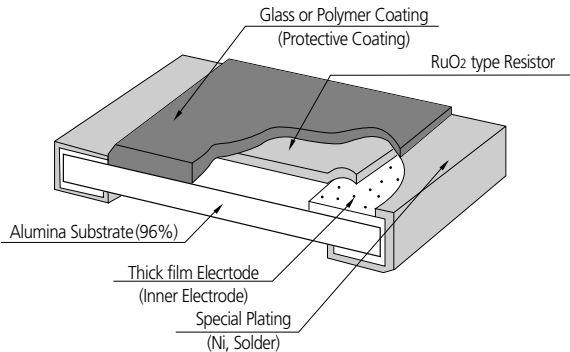
The product of lead-free terminal is RoHS compliant. PhO(lead oxide) is included in the glass of our product which is prescribed on RoHS appendix as an exception.

## Application

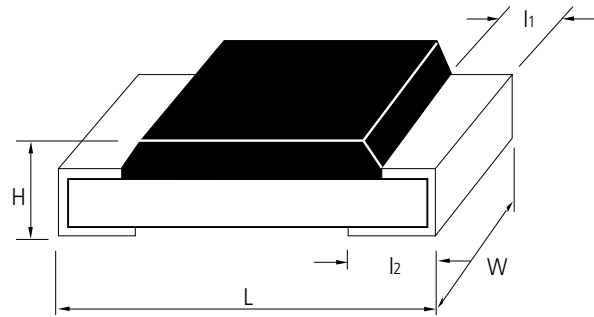
- Circuit for high precision resistance and reliability.
- For signal control part
- For tuning circuit.

## Structure and Dimensions

### • Structure



### • Dimensions



(UNIT: mm)

Type	Inch	Power(W)	L	W	H	l <sub>1</sub>	l <sub>2</sub>	Average Weight
RC 0603	0201	1/20	0.60 $\pm$ 0.03	0.30 $\pm$ 0.03	0.23 $\pm$ 0.03	0.10 $\pm$ 0.05	0.15 $\pm$ 0.05	0.15mg
RC 1005	0402	1/16	1.00 $\pm$ 0.05	0.50 $\pm$ 0.05	0.35 $\pm$ 0.05	0.20 $\pm$ 0.10	0.25 $\pm$ 0.10	0.6mg
RC 1608	0603	1/10	1.60 $\pm$ 0.10	0.80 $\pm$ 0.15	0.45 $\pm$ 0.10	0.30 $\pm$ 0.20	0.35 $\pm$ 0.10	2.1mg
RC 2012	0805	1/8	2.00 $\pm$ 0.20	1.25 $\pm$ 0.15	0.50 $\pm$ 0.10	0.40 $\pm$ 0.20	0.35 $\pm$ 0.20	4.9mg
RC 3216	1206	1/4	3.20 $\pm$ 0.20	1.60 $\pm$ 0.15	0.55 $\pm$ 0.10	0.45 $\pm$ 0.20	0.40 $\pm$ 0.20	9.5mg
RC 3225	1210	1/3	3.20 $\pm$ 0.20	2.55 $\pm$ 0.20	0.55 $\pm$ 0.10	0.45 $\pm$ 0.20	0.40 $\pm$ 0.20	16mg
RC 5025	2010	2/3	5.00 $\pm$ 0.20	2.50 $\pm$ 0.20	0.55 $\pm$ 0.10	0.60 $\pm$ 0.20	0.60 $\pm$ 0.20	26mg
RC 6432	2512	1	6.30 $\pm$ 0.20	3.20 $\pm$ 0.20	0.55 $\pm$ 0.10	0.60 $\pm$ 0.20	0.60 $\pm$ 0.20	41mg

## Parts Numbering System

- The part number system shall be in the following format

RC	1005	F	2370	CS
Code Designation	Dimension & Size Code	Tolerance	Resistance Value	Packaging Code
RC: Chip Resistor	0603: 0.6 $\times$ 0.3(mm) - 0201(inch) 1005: 1.0 $\times$ 0.5(mm) - 0402(inch) 1608: 1.6 $\times$ 0.8(mm) - 0603(inch) 2012: 2.0 $\times$ 1.2(mm) - 0805(inch) 3216: 3.2 $\times$ 1.6(mm) - 1206(inch) 3225: 3.2 $\times$ 2.5(mm) - 1210(inch) 5025: 5.0 $\times$ 2.5(mm) - 2010(inch) 6432: 6.4 $\times$ 3.2(mm) - 2512(inch)	F: $\pm 1\%$	3 or 4 digits coding system (IEC coding system) 3digits (E-24 series) 4digits (E-96 series)	GS: Bulk Packaging CS: Tape Packaging 7" ES: Tape Packaging 10" FS: Tape Packaging 13" AS: Tape Packaging 13"

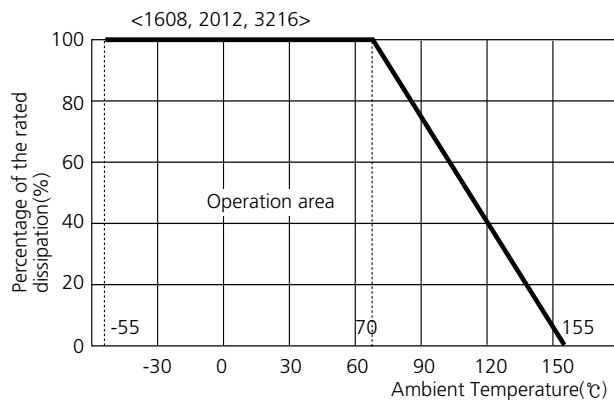
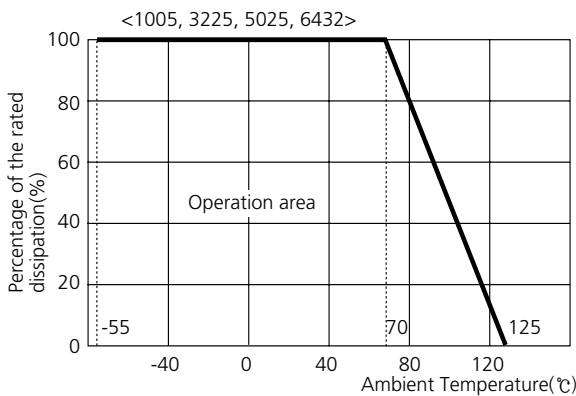
## Specification

Type	Power Rating (W)	Working Voltage (MAX)	Overload Voltage (MAX)	TCR (ppm/°C)	Resistance Range (Ω)	Rated Ambient Temperature	Rated Working Temperature
					F(±1%) E-96, E-24		
RC0603	1/20	25(V)	50(V)	±250ppm	1 Ω~10MΩ	70°C	-55°C~+125°C
RC 1005	1/16	50(V)	100(V)	±100ppm			
RC 1608	1/10		200(V)				400(V)
RC 2012	1/8	-55°C~+155°C					
RC 3216	1/4						
RC 3225	1/3	-55°C~+125°C					
RC 5025	2/3						
RC 6432	1						

- Rated voltage (V) =  $\sqrt{\text{Rated power(W)} \times \text{Normal resistance value (R)}}$   
Rated voltage should be lower than (MAX) working voltage.

## Power Derating Curve

The rated power is the maximum continuous loading power at 70°C ambient temperature.  
For ambient temperature above 70°C, the loading power follows the below power derating curve.



## Marking

• 3 digits indication (E-24 series)	• 4 digits indication (E-96 series)
<ul style="list-style-type: none"> <li>- Left 2 digits represent significant figures.</li> <li>- Last 1 digit represents exponential number of 10.</li> <li>- Example: <b>103</b> Left 2 digits: 10 Last 1 digit: 3 <math>103 = 10 \times 10^3 \Omega</math> <math>= 10000 \Omega = 10k\Omega</math></li> </ul>	<ul style="list-style-type: none"> <li>- Left 3 digits represent significant figures.</li> <li>- Last 1 digit represents exponential number of 10.</li> <li>- Example: <b>1002</b> Left 3 digits: 100 Last 1 digit: 2 <math>1002 = 100 \times 10^2 \Omega</math> <math>= 10000 \Omega = 10k\Omega</math></li> </ul>
• 0603, 1005 type: No marking.	• 0603, 1005, 1608 type: No marking.

## IEC Code System (E-96, E-24)

E-96	E-24	E-96	E-24	E-96	E-24	E-96	E-24
100	10	178		316		562	56
102		182	18	324	33	576	
105		187		332		590	
107		191		340		604	
110	11	196		348		619	
113		200	20	357	36	634	62
115		205		365		649	
118		210		374		665	
121	12	215		383	39	681	68
124		221	22	392		698	
127		226		402		715	
130	13	232		412		732	
133		237		422		750	75
137		243	24	432	43	768	
140		249		442		787	
143		255		453		806	
147		261		464		825	82
150	15	267		475	47	845	
154		274	27	487		866	
158		280		499		887	
162	16	287		511	51	909	
165		294		523		931	91
169		301	30	536		953	
174		309		549		976	

Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

Array

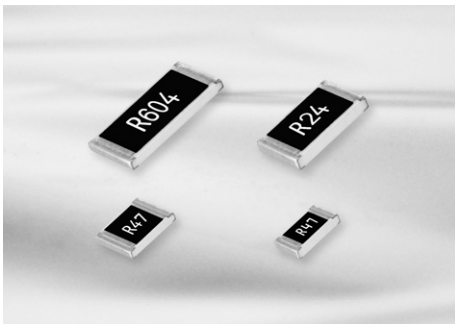
Attenuator

Characteristics Performance

Packaging

Standard Resistance Value

# Low Ohms



## Feature

- Under 1 ohms, precision resistance.
- Both flow and reflow soldering are applicable.
- Owing to the reduced lead inductance, the high frequency characteristic is excellent.

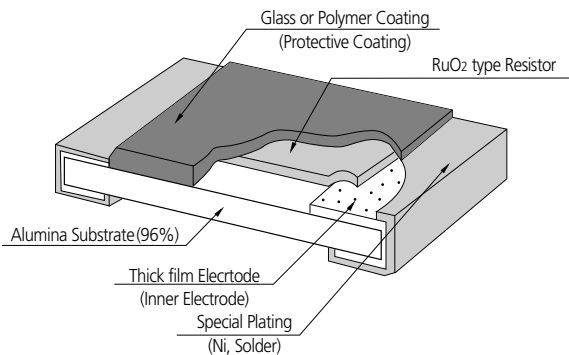
The product of lead-free terminal is RoHS compliant. PhO(lead oxide) is included in the glass of our product which is prescribed on RoHS appendix as an exception.

## Application

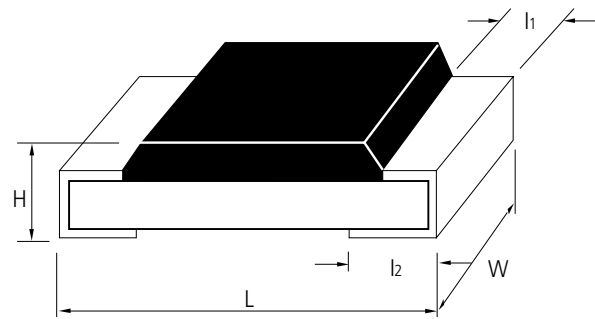
- Current detect.
- Safe circuit through protecting over-current flow.
- Power supplying part.
- Motor controlling, DC power charger, etc.

## Structure and Dimensions

### • Structure



### • Dimensions



(UNIT: mm)

Type	Inch	Power(W)	L	W	H	l <sub>1</sub>	l <sub>2</sub>	Average Weight
RC1005	0402	1/16	1.00±0.05	0.50±0.05	0.35±0.05	0.20±0.10	0.25±0.10	0.6mg
RC1608	0603	1/10	1.60±0.10	0.80±0.15	0.45±0.10	0.30±0.20	0.35±0.10	2.1mg
RC2012	0805	1/8	2.00±0.20	1.25±0.15	0.50±0.10	0.40±0.20	0.35±0.20	4.9mg
RC3216	1206	1/4	3.20±0.20	1.60±0.15	0.55±0.10	0.45±0.20	0.40±0.20	9.5mg
RC3225	1210	1/3	3.20±0.20	2.55±0.20	0.55±0.10	0.45±0.20	0.40±0.20	16mg
RC5025	2010	2/3	5.00±0.20	2.50±0.20	0.55±0.10	0.60±0.20	0.60±0.20	26mg
RC6432	2512	1	6.30±0.20	3.20±0.20	0.55±0.10	0.60±0.20	0.60±0.20	41mg

## Parts Numbering System

- The part number system shall be in the following format

RC	6432	J	R68	CS
Code Designation	Dimension & Size Code	Tolerance	Resistance Value	Packaging Code
RC: Chip Resistor	1005: 1.0 × 0.5(mm) - 0402(inch)	F: ±1%	3 or 4 digits coding system (E-24 series)	GS: Bulk Packaging
	1608: 1.6 × 0.8(mm) - 0603(inch)			CS: Tape Packaging 7"
	2012: 2.0 × 1.2(mm) - 0805(inch)	G: ±2%		ES: Tape Packaging 10"
	3216: 3.2 × 1.6(mm) - 1206(inch)			FS: Tape Packaging 13"
	3225: 3.2 × 2.5(mm) - 1210(inch)			AS: Tape Packaging 13"
5025: 5.0 × 2.5(mm) - 2010(inch)	J: ±5%			
6432: 6.4 × 3.2(mm) - 2512(inch)				

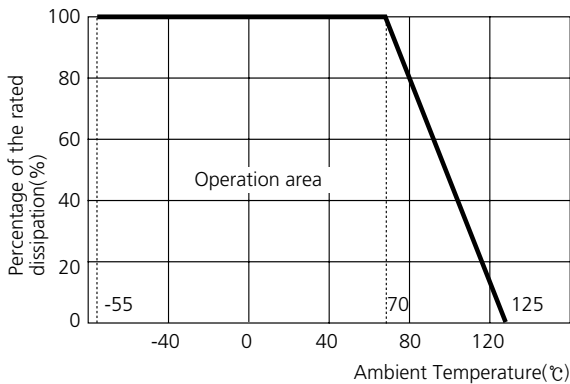
## Specification

Type	Power Rating (W)	Working Voltage (V, MAX)	Overload Voltage (V, MAX)	TCR (ppm/°C)	Resistance Range (Ω)			Rated Ambient Temperature	Rated Working Temperature
					F(±1%) E-24	G(±2%) E-24	J(±5%) E-24		
RC1005	1/16	0.25	0.63	0.1 Ω ≤ R < 0.15 Ω : 400(+300,-200) ppm/°C  0.15 Ω ≤ R < 1.0 Ω : 0~250 ppm/°C	0.1~0.98	70°C	-55°C~+125°C		
RC1608	1/10	0.32	0.79						
RC2012	1/8	0.35	0.88						
RC3216	1/4	0.50	1.25						
RC3225	1/3	0.58	1.44						
RC5025	2/3	0.82	2.04						
RC6432	1	1.00	2.50						

- Rated voltage (V) =  $\sqrt{\text{Rated power(W)} \times \text{Normal resistance value (R)}}$   
Rated voltage should be lower than (MAX) working voltage.

## Power Derating Curve

The rated power is the maximum continuous loading power at 70°C ambient temperature. For ambient temperature above 70°C, the loading power follows the below power derating curve.



## Marking

### E-24 series

#### 3 digits indication

- R means decimal point.
- Other digits represent significant value.
- Example: **R22**  
Left 1 digit: R  
Last 2 digits: 22  
**R22 = 0.22 Ω**



#### 4 digits indication

- R means decimal point.
- Other digits represent significant value.
- Example: **R075**  
Left 1 digit: R  
Last 3 digits: 075  
**R075 = 0.075 Ω**



## Resistance Value Table

- E-24 series

Code	R-value	Code	R-value	Code	R-value
R039	0.039 Ω	R10	0.10 Ω	R33	0.33 Ω
R043	0.043 Ω	R11	0.11 Ω	R36	0.36 Ω
R047	0.047 Ω	R12	0.12 Ω	R39	0.39 Ω
R051	0.051 Ω	R13	0.13 Ω	R43	0.43 Ω
R056	0.056 Ω	R15	0.15 Ω	R47	0.47 Ω
R062	0.062 Ω	R16	0.16 Ω	R51	0.51 Ω
R068	0.068 Ω	R18	0.18 Ω	R56	0.56 Ω
R075	0.075 Ω	R20	0.20 Ω	R62	0.62 Ω
R082	0.082 Ω	R22	0.22 Ω	R68	0.68 Ω
R091	0.091 Ω	R24	0.24 Ω	R75	0.75 Ω
		R27	0.27 Ω	R82	0.82 Ω
		R30	0.30 Ω	R91	0.91 Ω

Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

Array

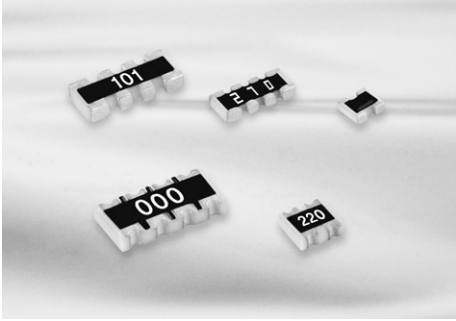
Attenuator

Characteristics Performance

Packaging

Standard Resistance Value

# Array



## Feature

- Reducing SMD surface area (40% reduced).
- Reducing SMD costs (75% reduced).
- Both flow and reflow soldering are applicable.
- Convex & concave type.

The product of lead-free terminal is RoHS compliant. PhO(lead oxide) is included in the glass of our product which is prescribed on RoHS appendix as an exception.

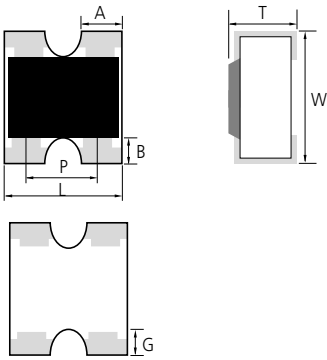
## Application

- For semiconductor devices.
- For computers, digital circuits.

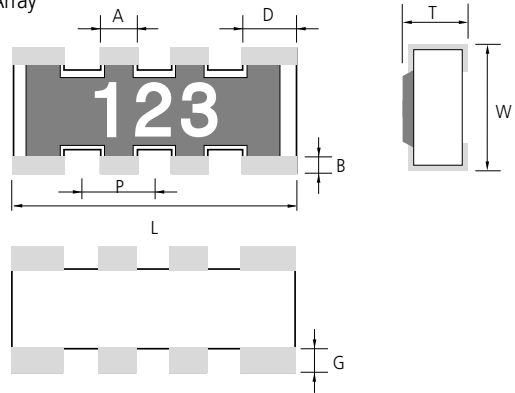
## Structure and Dimensions

### (1) CONVEX TERMINAL TYPE

• 2 Array



• 4 Array

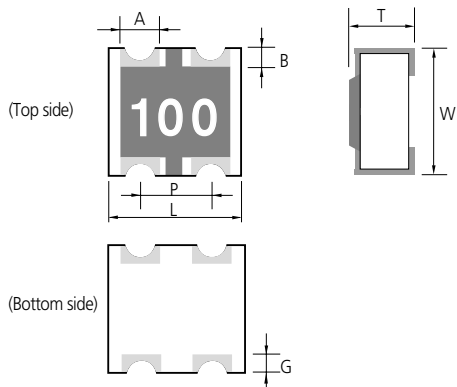


(UNIT: mm)

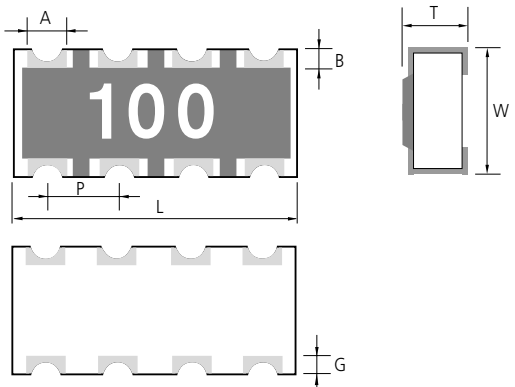
Type	L	W	T	A	D	B	G	P	Average Weight
RP102P	1.00±0.10	1.00±0.10	0.35±0.10	0.33±0.05	-	0.20±0.10	0.25±0.10	0.65±0.10	1.1mg
RP104P	2.00±0.10	1.00±0.10	0.35±0.10	0.30±0.15	0.40±0.15	0.15±0.10	0.25±0.15	0.50±0.15	2.2mg
RP164P	3.20±0.10	1.60±0.10	0.50±0.10	0.40±0.15	0.60±0.15	0.30±0.15	0.30±0.15	0.80±0.15	8.9mg

### (2) CONCAVE TERMINAL TYPE

• 2 Array



• 4 Array



(UNIT: mm)

Type	L	W	T	A	B	G	P	Unit Weight
RN102P	1.00±0.10	1.00±0.10	0.35±0.10	0.30±0.10	0.15±0.10	0.25±0.15	0.5±0.10	1.2mg
RN104P	2.00±0.10	1.00±0.10	0.40±0.10	0.30±0.10	0.15±0.10	0.25±0.15	0.5±0.10	2.8mg
RN162P	1.60±0.10	1.60±0.10	0.60±0.10	0.45±0.10	0.30±0.15	0.40±0.15	0.80±0.10	4.7mg
RN164P	3.20±0.10	1.60±0.10	0.60±0.10	0.45±0.10	0.30±0.15	0.40±0.15	0.80±0.10	9.9mg

## Parts Numbering System

- The part number system shall be in the following format

RN	16	4P	J	100	FS
Code Designation	Dimension	Resistors	Tolerance	Resistance Value	Packaging Code
RP: Convex type array RN: Concave type array	10: 1005 16: 1608	2P: 2 Pieces 4P: 4 Pieces	J: $\pm 5\%$ * Jumper: 'J'	3 digit coding system (IEC coding system) E-24 series	CS : Tape Packaging 7" ES : Tape Packaging 10" FS : Tape Packaging 13" AS : Tape Packaging 13"

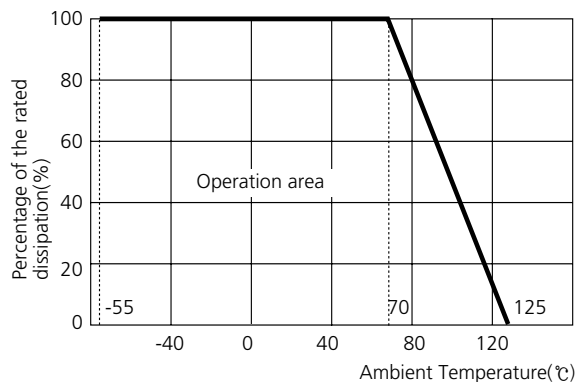
## Specification

Type	Power Rating (W)	Working Voltage (MAX)	Overload Voltage (MAX)	TCR (ppm/°C)	Resistance Range ( $\Omega$ )	Rated Ambient Temperature	Rated Working Temperature
102P, 104P 162P, 164P	1/16	25(V) 50(V)	50(V) 100(V)	$\pm 250$ ppm $\pm 200$ ppm	1 $\Omega$ ~ 1M $\Omega$	70°C	-55°C ~ +125°C

- Rated voltage (V) =  $\sqrt{\text{Rated power(W)} \times \text{Normal resistance value (R)}}$   
Rated voltage should be lower than (MAX) working voltage.

## Power Derating Curve

The rated power is the maximum continuous loading power at 70°C ambient temperature. For ambient temperature above 70°C, the loading power follows the below power derating curve. (The load current shall be derated according to Derating curve in case of the 'Jumper')

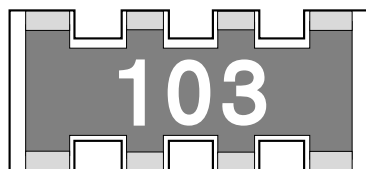


## Jumper Resistors

Type	Resistance	Current Rating	Rated Ambient Temperature	Rated Working Temperature
102P, 104P 162P, 164P	50m $\Omega$ Max.	1.0(A)	70°C	-55°C ~ +125°C

## Marking

- 3 digits indication(E-24 series)
  - Left 2 digits represent significant figures.
  - Last 1 digit represents exponential number of 10.
  - Example: 103
    - Left 2 digit: 10
    - Last 1 digit: 3
    - $103 = 10 \times 10^3 = 10000 \Omega = 10k\Omega$



\* Jumper chip is printed as "000".

- RP102P, RN102P, RN104P type : No marking.

Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

Array

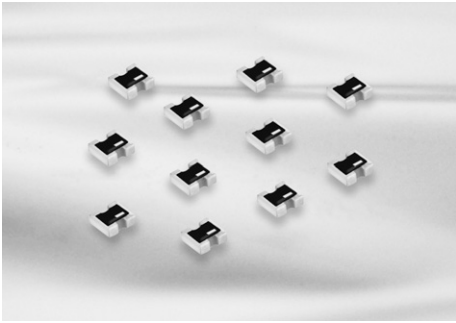
Attenuator

Characteristics Performance

Packaging

Standard Resistance Value

# Attenuator



## Feature

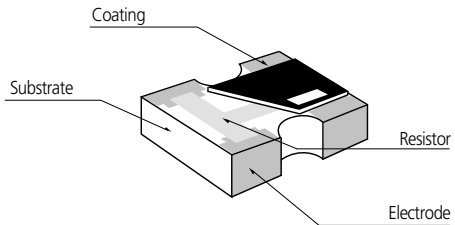
- The RP10AT is small-size chip Attenuator, suitable for high density surface mounting.
- Unbalanced  $\pi$  type attenuator circuit in one chip(1.0 mm x 1.0 mm)
- Mounting occupation area reduction : about 50 % reduction
- Mounting cost reduction : Mounting times 3 times  $\rightarrow$  1 time
- Attenuation : 0 dB to 10 dB

## Application

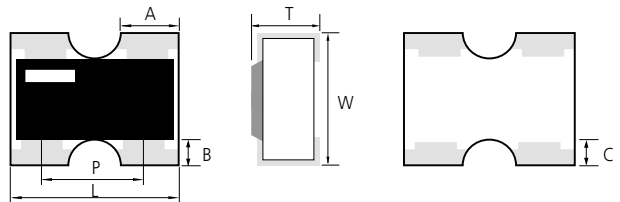
- Attenuation / level control / impedance matching of high frequency signals of communication equipment; cellular phones(GSM, CDMA, etc.), PHS, PDA, for example.

## Structure and Dimensions

### • Structure



### • Dimensions



(UNIT: mm)

Type	Power(W)	L	W	T	A	B	C	P	Average Weight
RP10AT	0.04W / package	1.00 $\pm$ 0.10	1.00 $\pm$ 0.10	0.35 $\pm$ 0.10	0.33 $\pm$ 0.05	0.20 $\pm$ 0.10	0.25 $\pm$ 0.10	0.65 $\pm$ 0.10	1.1mg

## Parts Numbering System

- The part number system shall be in the following format

RP	10AT	L	A	03	CS
Code Designation	Dimensions & Circuit Configuration	Attenuation Value Tolerance	Characteristics Impedance	Attenuation Value	Packing Type
RP: Convex type	10: 1.0x1.0(mm)-0404(inch) AT: Unbalanced $\pi$ -type Attenuator	L: $\pm$ 0.3 dB H: $\pm$ 0.5 dB	A: 50 ohm	3 dB EX) 0 $\rightarrow$ 0dB	CS: Tape Packaging 7"

## Specification

Item	Specifications
Attenuation Value	0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB, 6 dB, 7 dB, 8 dB, 9 dB, 10dB
Attenuation Value Tolerance	0 dB, 1 dB, 2 dB, 3 dB, 4 dB, 5 dB : $\pm 0.3$ dB 6 dB, 7 dB, 8 dB, 9 dB, 10dB : $\pm 0.5$ dB
Characteristic Impedance	50 $\Omega$
Power Rating	0.04W / package
Frequency Range	DC to 3 GHz
VSWR (Voltage Standing Wave Ratio)	1.3 max
Number of terminals	4 terminals
Category Temperature Range (Operating Temperature Range)	-55 $^{\circ}$ C to +125 $^{\circ}$ C

Operation  
Notes

Example of Land  
Pattern Design

Recommended  
Soldering Conditions

General  
Purpose

Precision

Low Ohms

Array

**Attenuator**

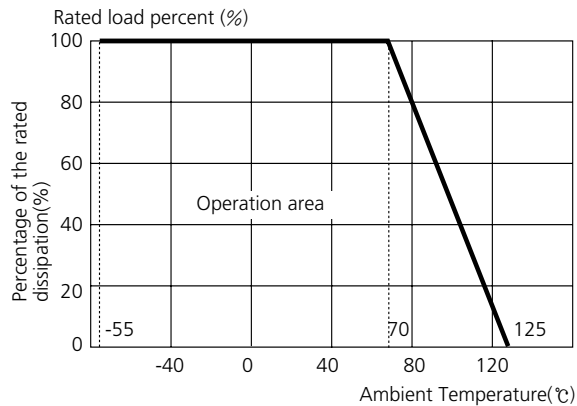
Characteristics  
Performance

Packaging

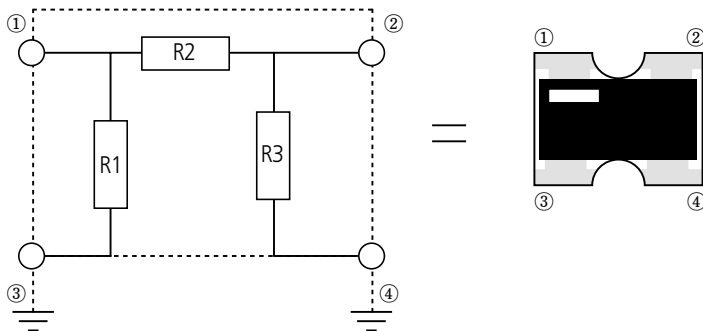
Standard  
Resistance Value

## Power Derating Curve

The rated power is the maximum continuous loading power at 70 $^{\circ}$ C ambient temperature.  
For ambient temperature above 70 $^{\circ}$ C, the loading power follows the below power derating curve.

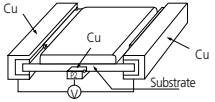


## Equivalent Circuit Configuration

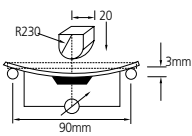



# Characteristics Performance

## Electrical Characteristic

Item	Requirements Specification		Test Methods		
	Resistor	Jumper	Resistor	Jumper	
Direct Current Resistance	Within the regulated resistance tolerance.	50mΩ Max.	JIS C 5201-1 4.5 Voltage apply Within 5 sec. Test temp: 20℃, 65RH Test board: <FIG. 1>		
Temperature Characteristic	<ul style="list-style-type: none"> <li>■ J-Grade</li> <li>1Ω ≤ R &lt; 10Ω : +300/-200ppm/℃</li> <li>10Ω ≤ R ≤ 1MΩ : ±100ppm/℃ (0603±250ppm)</li> <li>1MΩ &lt; R ≤ 10MΩ : ±300ppm/℃</li> <li>■ F-Grade</li> <li>1Ω ≤ R ≤ 10MΩ : ±100ppm/℃ (0603±250ppm)</li> </ul>		JIS C 5201-1 4.8 Test Temperature(℃) 20℃ → -55℃ / 20℃ → 125℃ T.C.R(ppm / ℃) = (R-R <sub>20</sub> ) / R <sub>20</sub> × 1 / (T-T <sub>20</sub> ) × 10 <sup>6</sup> ※ T=test Temperature, T <sub>20</sub> =20℃ R=Resistance at T, R <sub>20</sub> =Resistance at T <sub>20</sub> Test board: <FIG. 1>		
Shot-time Overload	ΔR	Less than ±(1%+0.1Ω) of the initial value.	50mΩ Max.	JIS C 5201-1 4.13 Apply 2.5 times rated voltage for 5 sec. Wait 60 minutes at room temperature. Measure the resistance value. Test board: <FIG. 1>	Max Surge Current
	Visual	No evidence of mechanical damage.			
Intermittent Overload	ΔR	Less than ±(3%+0.1Ω) of the initial value.	50mΩ Max.	JIS C 5201-1 4.13 2.5 times of rated voltage . 1 second ON, 25 second OFF. 10,000 cycles. Test board: <FIG. 1>	Max Surge Current
	Visual	No evidence of mechanical damage.			
Dielectric Withstanding Voltage	No evidence of mechanical damage.		JIS C 5201-1 4.7 Apply Voltage for 1 minute		
Insulation Resistance	Over 1,000MΩ		0603: 50V 1005, 1608: 100V Other: 500V		

## Mechanical Characteristic

Item	Requirements Specification		Test Methods	
	Resistor	Jumper	Resistor	Jumper
Solderability	Coverage: ≥95% each termination. No crack of termination parts and ceramic exposure of surface by melting.		IEC60068-2-58 Rosin Flux: Rosin 25%, Methanol 75% Solder Temp.: 245±5/-0℃ Dipping time: 2±0.5 sec.(Both side dipping)	
Bending Test	ΔR	Less than ±(0.5%+0.05Ω) of the initial value.	50mΩ Max.	
	Visual	No evidence of mechanical damage.		
Adhesive strength of termination	· No mechanical damage or sign of disconnection		JIS C 5201-1(4.16) - Test strength : 5N (500g · f), 0603 : 2N - Test time : Applying pressure for 10 seconds	
Resistance to Soldering Heat	ΔR	Less than ±(1%+0.05Ω) of the initial value.	50mΩ Max.	JIS C 5201-1 4.18 - Flow soldering : 260±5℃, 10 sec. max.(both side dipping) - Reflow soldering : 260±5℃, 10 sec. max. over 230℃, 30~40 sec.
	Visual	No evidence of mechanical damage.		
Anti-Vibration Test	ΔR	Less than ±(1%+0.05Ω) of the initial value.	50mΩ Max.	JIS C 5201-1 4.22 2 hours each in X, Y and Z axis(total 6 hours) 10 to 55Hz sweep in 1 minute at 1.5mm amplitude.
	Visual	No evidence of mechanical damage.		

## Environmental Characteristic

Item	Requirements Specification		Test Methods	
	Resistor	Jumper	Resistor	Jumper
Temperature Cycle	$\Delta R$	Less than $\pm(1\%+0.1 \Omega)$ of the initial value.	50m $\Omega$ Max.	JIS C 5201-1 4.19 Perform 100 cycles as follows. Test Condition: -55 °C/30min $\leftrightarrow$ 125 °C/30min sweep time: 5 min Test board: <FIG. 1>
	Visual	No evidence of mechanical damage.		
Load Life	$\Delta R$	Less than $\pm(3\%+0.1 \Omega)$ of the initial value.	50m $\Omega$ Max.	JIS C 5201-1 4.25 Test Voltage: rated voltage Test temp.: 70 $\pm$ 2 °C Time: 1,000 <sup>+48</sup> hours(90 min; ON, 30 min; OFF) Test board: <FIG. 1>
	Visual	No evidence of mechanical damage.		
Low Temp. Exposure	$\Delta R$	Less than $\pm(3\%+0.1 \Omega)$ of the initial value.	50m $\Omega$ Max.	JIS C 5201-1 4.23 Dwell in -55 °C chamber without loading for 1,000 <sup>+48</sup> hours. Stabilize for 60 minute at room temperature. Measure value. Test board: <FIG. 1>
	Visual	No evidence of mechanical damage.		
High Temp. Exposure	$\Delta R$	Less than $\pm(3\%+0.1 \Omega)$ of the initial value.	50m $\Omega$ Max.	JIS C 5201-1 4.23 Dwell in 125 °C $\pm$ 2 °C or 155 °C $\pm$ 2 °C chamber without loading for 1,000 <sup>+48</sup> hours. Stabilize for 60 minute at room temperature. Measure value. Test board: <FIG. 1>
	Visual	No evidence of mechanical damage.		
Moisture Resistance	$\Delta R$	Less than $\pm(3\%+0.1 \Omega)$ of the initial value.	50m $\Omega$ Max.	JIS C 5201-1 4.14 Test Voltage: rated voltage Test temp.: 40 $\pm$ 2 °C Time: 1,000 <sup>+48</sup> hours(90 min; ON, 30 min; OFF) Humidity: 90~95% RH Stabilize for 1 hrs & Measure. Test board: <FIG. 1>
	Visual	No evidence of mechanical damage.		

\* These characteristics apply to 1  $\Omega$  ~ 10M  $\Omega$ . In case of other resistance range, please contact us.

\* The next is specification in our company for flow soldering and test boards.

## Flow soldering Conditions

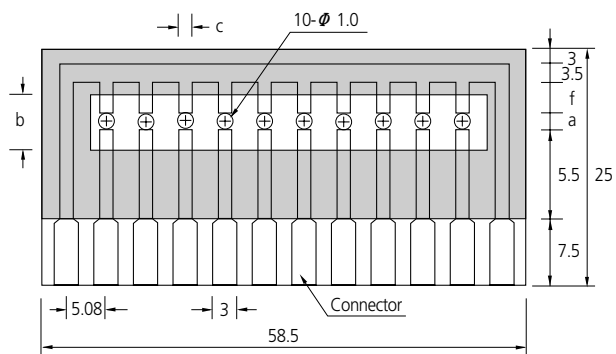
Item	Specification	Dipping
Flux	ROSIN 25%, IPA 75%	Time: 5~10 sec.
Solder	Sn-3.0Ag-0.5Cu	Time: 10 sec max. Temp.: 260 $\pm$ 5 °C.

## Test Board

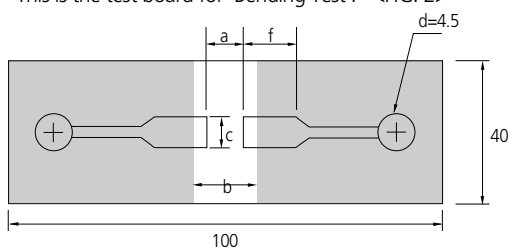
- This is the test board in order to test 'Direct Current Resistance', 'Temperature Characteristic', 'Short-time Overload', 'Temperature Cycle', 'Load Life', 'Low Temperature Exposure', and so on.

<FIG. 1>

Description	Dimension(mm)				
	Type	Power	a	b	c
RC 0603	1/20	0.3	1.1	0.45	5.2
RC 1005	1/16	0.6	1.9	0.7	4.9
RC 1608	1/10	1.0	3.0	1.2	4.5
RC 2012	1/8	1.2	4.0	1.65	4.3
RC 3216	1/4	2.2	5.0	2.0	3.3
RC 3225	1/3	2.2	5.0	2.9	3.3
RC 5025	2/3	3.6	7.0	3.0	3.0
RC 6432	1	5.2	8.0	3.5	2.5



- This is the test board for 'Bending Test'. <FIG. 2>



Description	Dimension(mm)				
	Type	Power	a	b	c
RC 0603	1/20	0.3	1.1	0.45	5.2
RC 1005	1/16	0.6	1.9	0.7	4.9
RC 1608	1/10	1.0	3.0	1.2	4.5
RC 2012	1/8	1.2	4.0	1.65	4.3
RC 3216	1/4	2.2	5.0	2.0	3.3
RC 3225	1/3	2.2	5.0	2.9	3.3
RC 5025	2/3	3.6	7.0	3.0	3.0
RC 6432	1	5.2	8.0	3.5	2.5

Design and Specifications are subject to change without prior notice.  
Please ask the manufacturer for technical Specifications before you order and/or use.

Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

Array

Attenuator

Characteristics Performance

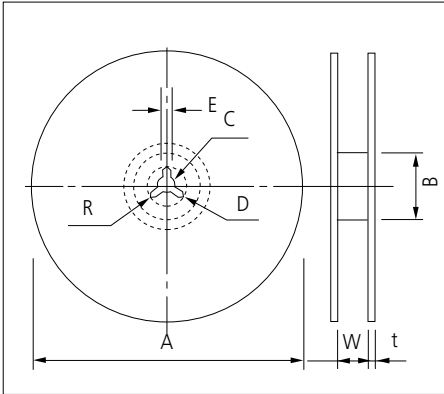
Packaging

Standard Resistance Value

## Taping Type

• Reel dimensions

(UNIT: mm)



Symbol	Tape Width	A	B	C	D
7" Reel	8mm	$\varnothing 180 \pm 0/-3$	$\varnothing 60 \pm 1/0$	$\varnothing 13 \pm 0.3$	$\varnothing 25 \pm 0.5$
	12mm	$\varnothing 180 \pm 0/-3$	$\varnothing 60 \pm 1/0$	$\varnothing 13 \pm 0.3$	$\varnothing 25 \pm 0.5$
10" Reel	8mm	$\varnothing 258 \pm 2.0$	$\varnothing 80 \pm 1.0$	$\varnothing 13 \pm 0.3$	$\varnothing 25 \pm 0.5$
	12mm	$\varnothing 330 \pm 2.0$	$\varnothing 80 \pm 1.0$	$\varnothing 13 \pm 0.3$	$\varnothing 25 \pm 0.5$

Symbol	Tape Width	E	W	t	R
7" Reel	8mm	$2.0 \pm 0.5$	$9 \pm 0.5$	$1.2 \pm 0.2$	1.0
	12mm	$2.0 \pm 0.5$	$13 \pm 0.5$	$1.2 \pm 0.2$	1.0
10" Reel	8mm	$2.0 \pm 0.5$	$9 \pm 0.5$	$1.8 \pm 0.2$	1.0
13" Reel	8mm	$2.0 \pm 0.5$	$9 \pm 0.5$	$2.2 \pm 0.2$	1.0
	12mm	$2.0 \pm 0.5$	$13 \pm 0.5$	$2.2 \pm 0.2$	1.0

• Tape dimensions

(UNIT: mm)

Type	Pitch	Width	Dimensions																																
Press Pocket or Punched Paper	2mm	8mm	<table border="1"> <thead> <tr> <th></th> <th>A</th> <th>B</th> <th>T</th> </tr> </thead> <tbody> <tr> <td>0603</td> <td><math>0.38 \pm 0.05</math></td> <td><math>0.68 \pm 0.05</math></td> <td>0.5 Max</td> </tr> <tr> <td>1005</td> <td><math>0.70 \pm 0.10</math></td> <td><math>1.20 \pm 0.10</math></td> <td>0.6 Max</td> </tr> <tr> <td>RP102</td> <td><math>1.17 \pm 0.10</math></td> <td><math>1.17 \pm 0.10</math></td> <td>0.6 Max</td> </tr> <tr> <td>RP10AT</td> <td><math>1.20 \pm 0.10</math></td> <td><math>1.20 \pm 0.10</math></td> <td>0.6 Max</td> </tr> <tr> <td>RN102</td> <td><math>1.20 \pm 0.10</math></td> <td><math>1.20 \pm 0.10</math></td> <td>0.6 Max</td> </tr> <tr> <td>RP104</td> <td><math>1.20 \pm 0.10</math></td> <td><math>2.20 \pm 0.10</math></td> <td>0.6 Max</td> </tr> <tr> <td>RN104</td> <td><math>1.20 \pm 0.10</math></td> <td><math>2.20 \pm 0.10</math></td> <td>0.8 Max</td> </tr> </tbody> </table> <p>-0603: Press pocket.</p>		A	B	T	0603	$0.38 \pm 0.05$	$0.68 \pm 0.05$	0.5 Max	1005	$0.70 \pm 0.10$	$1.20 \pm 0.10$	0.6 Max	RP102	$1.17 \pm 0.10$	$1.17 \pm 0.10$	0.6 Max	RP10AT	$1.20 \pm 0.10$	$1.20 \pm 0.10$	0.6 Max	RN102	$1.20 \pm 0.10$	$1.20 \pm 0.10$	0.6 Max	RP104	$1.20 \pm 0.10$	$2.20 \pm 0.10$	0.6 Max	RN104	$1.20 \pm 0.10$	$2.20 \pm 0.10$	0.8 Max
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Punched Paper	4mm	8mm	<table border="1"> <thead> <tr> <th></th> <th>A</th> <th>B</th> <th>T</th> </tr> </thead> <tbody> <tr> <td>1608</td> <td><math>1.10 \pm 0.20</math></td> <td><math>1.90 \pm 0.20</math></td> <td>0.8 Max</td> </tr> <tr> <td>2012</td> <td><math>1.65 \pm 0.20</math></td> <td><math>2.40 \pm 0.20</math></td> <td rowspan="5">1.1 Max</td> </tr> <tr> <td>3216</td> <td><math>2.00 \pm 0.20</math></td> <td><math>3.60 \pm 0.20</math></td> </tr> <tr> <td>3225</td> <td><math>2.90 \pm 0.20</math></td> <td><math>3.60 \pm 0.20</math></td> </tr> <tr> <td>RN162</td> <td><math>1.90 \pm 0.20</math></td> <td><math>1.90 \pm 0.20</math></td> </tr> <tr> <td>RP164</td> <td><math>2.00 \pm 0.20</math></td> <td><math>3.60 \pm 0.20</math></td> </tr> <tr> <td>RN164</td> <td><math>2.00 \pm 0.20</math></td> <td><math>3.60 \pm 0.20</math></td> <td></td> </tr> </tbody> </table>		A	B	T	1608	$1.10 \pm 0.20$	$1.90 \pm 0.20$	0.8 Max	2012	$1.65 \pm 0.20$	$2.40 \pm 0.20$	1.1 Max	3216	$2.00 \pm 0.20$	$3.60 \pm 0.20$	3225	$2.90 \pm 0.20$	$3.60 \pm 0.20$	RN162	$1.90 \pm 0.20$	$1.90 \pm 0.20$	RP164	$2.00 \pm 0.20$	$3.60 \pm 0.20$	RN164	$2.00 \pm 0.20$	$3.60 \pm 0.20$					
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Embossed Tape		12mm	<table border="1"> <thead> <tr> <th></th> <th>A</th> <th>B</th> <th>T</th> </tr> </thead> <tbody> <tr> <td>5025</td> <td><math>2.80 \pm 0.20</math></td> <td><math>5.30 \pm 0.20</math></td> <td rowspan="2">1.1 Max</td> </tr> <tr> <td>6432</td> <td><math>3.50 \pm 0.20</math></td> <td><math>6.75 \pm 0.20</math></td> </tr> </tbody> </table>		A	B	T	5025	$2.80 \pm 0.20$	$5.30 \pm 0.20$	1.1 Max	6432	$3.50 \pm 0.20$	$6.75 \pm 0.20$																					
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## Packaging Table

TYPE	inch	Taping Packaging				
		Packaging Code	Reels	Tape	Quantity	Average Weight (g)
0603	0201	CS	7"	Press pocket	15,000	140
		AS	13"		60,000	570
1005	0402	CS	7"	Punched Paper	10,000	101
		ES	10"		30,000	379
		AS	13"		40,000	507
1608	0603	CS	7"	Punched Paper	5,000	123
		ES	10"		10,000	350
		FS	13"		15,000	497
		AS			20,000	580
2012	0805	CS	7"	Punched Paper	5,000	150
		ES	10"		10,000	397
		FS	13"		15,000	565
		AS			20,000	679
3216	1206	CS	7"	Punched Paper	5,000	165
		ES	10"		10,000	417
		FS	13"		15,000	605
		AS			20,000	734
3225	1210	CS	7"	Punched Paper	5,000	188
		ES	10"		10,000	463
		FS	13"		15,000	674
		AS			20,000	825
5025	2010	CS	7"	Embossed Tape	4,000	200
6432	2512	CS	7"	Embossed Tape	4,000	260
		AS	13"		15,000	1,041
102P	0402×2	CS	7"	Punched Paper	10,000	110
		AS	13"		40,000	495
104P	0402×4	CS	7"	Punched Paper	10,000	135
		AS	13"		40,000	615
162P	0603×2	CS	7"	Punched Paper	5,000	160
164P	0603×4	CS	7"	Punched Paper	5,000	163
		ES	13"		15,000	1,041
		AS			20,000	713

- General type, Precision, Low ohms, High ohms.
- Packaging style can be modified when you want.

Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

Array

Attenuator

Characteristics Performance

Packaging

Standard Resistance Value

# Standard Resistance Value

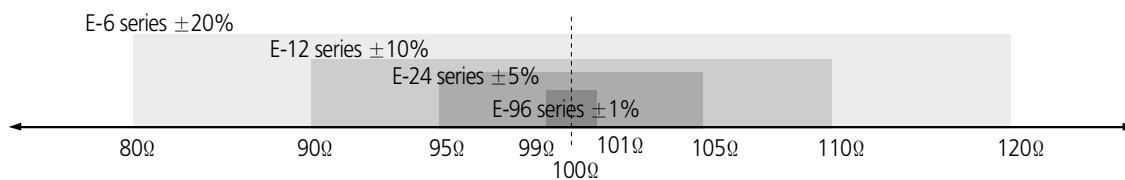
## Tolerance Code Table

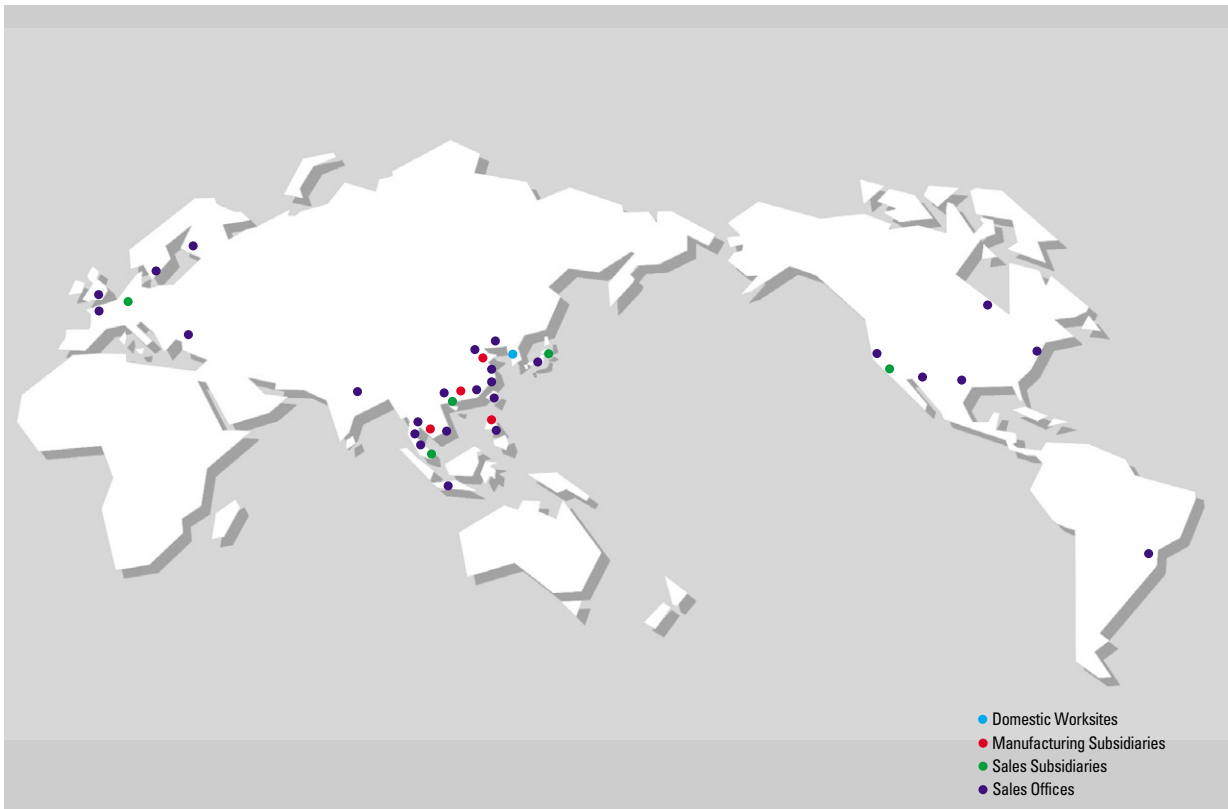
Tolerance Code	D	F	G	J	K	M
Digit Number	4 digit			3 digit		
IEC-Code System	E-192	E-96	E-48	E-24	E-12	E-6
Specification	±0.5%	±1%	±2%	±5%	±10%	±20%

## Significant Figure of Resistance Value

E-192	E-96	E-48	E-24	E-192	E-96	E-48	E-24	E-192	E-96	E-48	E-24	E-192	E-96	E-48	E-24
100	100	100	10	178	178	178		316	316	316		562	562	562	56
101				180			18	320				569			
102	102			182	182			324	324			576	576		
104				184				328				583			
105	105	105		187	187	187		332	332	332	33	590	590	590	
106				189				336				597			
107	107			191	191			340	340			604	604		
109				193				344				612			
110	110	110	11	196	196	196		348	348	348		619	619	619	
111				198				352				626			62
113	113			200	200		20	357	357			634	634		
114				203				361				642			
115	115	115		205	205	205		365	365	365	36	649	649	649	
117				208				370				657			
118	118			210	210			374	374			665	665		
120			12	213				379				673			
121	121	121		215	215	215		383	383	383		681	681	681	68
123				218				388				690			
124	124			221	221		22	392	392		39	698	698		
126				223				397				706			
127	127	127		226	226	226		402	402	402		715	715	715	
129				229				407				723			
130	130		13	232	232			412	412			732	732		
132				234				417				741			
133	133	133		237	237	237		422	422	422		750	750	750	75
135				240			24	427				759			
137	137			243	243			432	432		43	768	768		
138				246				437				777			
140	140	140		249	249	249		442	442	442		787	787	787	
142				252				448				796			
143	143			255	255			453	453			806	806		
145				258				459				816			
147	147	147		261	261	261		464	464	464		825	825	825	82
149				264				470			47	835			
150	150		15	267	267			475	475			845	845		
152				271			27	481				856			
154	154	154		274	274	274		487	487	487		866	866	866	
156				277				493				876			
158	158			280	280			499	499			887	887		
160			16	284				505				898			
162	162	162		287	287	287		511	511	511	51	909	909	909	
164				291				517				920			91
165	165			294	294			523	523			931	931		
167				298				530				942			
169	169	169		301	301	301	30	536	536	536		953	953	953	
172				305				542				965			
174	174			309	309			549	549			976	976		
176				312				556				988			

• Example





Operation Notes

Example of Land Pattern Design

Recommended Soldering Conditions

General Purpose

Precision

Low Ohms

Array

Attenuator

Characteristics Performance

Packaging

Standard Resistance Value

### 국내대리점<가나다順>

#### 삼테크

서울시 강남구 대치동 1001-13, 14, 15  
 TEL : (02)3458-9000 FAX : (02)3458-9100

#### 에이펙스인텍

서울시 금천구 가산동 371-28  
 우림라이온스 빌리 C동 905호  
 TEL : (02)2026-2610-2 FAX : (02)2026-2613

#### 청맥전자

서울시 용산구 원효로 3가 51-31  
 전자랜드 별관 2층 246호 140-113  
 TEL : (02)716-6428 FAX : (02)719-8325

#### 청한전자

서울시 용산구 한강로 3가 16-96 의림빌딩 3층  
 TEL : (02)718-3322 FAX : (02)703-8958

#### 균칩

서울시 금천구 가산동 219-8B동 2층  
 TEL : (02)838-5588 FAX : (02)859-5858





## Passive components Sales Office

### • Head Office

206, Cheomdansaneop Road,  
Youngtong-gu,  
Suwon, Kyonggi Province 443-743,  
Korea

#### Europe

Tel: +82-31-210-6328  
E-mail: james.pyun@samsung.com

#### America

Tel: +82-31-210-6794  
E-mail: randy.kim@samsung.com

#### Asia

Tel: +82-31-210-5348  
E-mail: koogi@samsung.com

#### Domestic

Tel: +82-31-210-3757  
E-mail: southjoy@samsung.com

### • Manufacturing Site

#### Suwon Plant (Korea)

206 Cheomdansaneop Road,  
Youngtong-gu,  
Suwon, Kyonggi Province 443-743,  
Korea  
Tel: +82-31-210-6794  
E-mail: randy.kim@samsung.com

#### Busan Plant (Korea)

1623-2, Songjeong-dong,  
Kangseo-gu, Busan 618-270, Korea  
Tel: +82-51-970-7671  
E-mail: kyc.kweon@samsung.com

#### Tianjin Plant (China)

27, Heiniucheng-Road, Hexi District,  
Tianjin, China 300210  
Tel: +86-22-2830-3333(3450)  
E-mail: gk.ryu@samsung.com

#### Philippines Plant (Philippines)

Calamba Premiere International Park,  
Batino, Calamba, Laguna, Manila  
Tel: +63-2-809-2873  
E-mail: ksj1445@samsung.com

### • Asia sales office

#### Shanghai Office

Rm 1408 Shanghai international  
trade center No 2200 Yan an(W) RD  
Shanghai China 200335  
Tel: 86-21-6270-4168(274)  
E-mail: dennis.cha@samsung.com

#### Shenzhen Office

Rm 4501, 45/F, New World Center,  
Yitian Road, Futian District,  
Shenzhen, China 518026  
Tel: 86-755-8608-5581  
E-mail: jackson.xian@samsung.com

#### Qingdao Office

Rm 1201, Growne Plaza Qingdao;  
76XiangGangZhong Rd, Qingdao;266071  
P.R. China  
Tel: 86-532-5779102  
E-mail: zhengguo.cui@samsung.com

#### HongKong Office

Suite 4511, Two int'l Finance Centre,  
8 Finance Street, Central, Hongkong  
Tel: 852-2862-6350  
E-mail: vinsent.chou@samsung.com

#### Singapore Office

3 Church Street Samsung Hub  
#23-02 Singapore 049483  
Tel: (65)6833-3228  
E-mail: winson.yeong@samsung.com

#### Thai Office

Wellgrow Industrial Estate,  
93 Moo 5 T. Bangsamak, A.Bangpakong  
Chachoengsao 24180 Thailand  
Tel: 66-38-562-026  
E-mail: sbimm@samsung.com

#### Taiwan Office

399 9F-1, Ruey Kuang Rd., Neihu,  
Taipei, Taiwan  
Tel: 886-2-2656-8356  
E-mail: kevin0130.wang@samsung.com

### • America sales office

#### Irvine Office

3345 Michelson Drive,  
Suite 350, Irvine, CA 92612  
Tel: 1-949-797-8047  
E-mail: sh386.kim@samsung.com

### • Europe sales office

#### Frankfurt Office

Samsung haus  
Am Kronberger Hang 6  
D-65824 Schwalbach/Ts.  
Tel: 49-6196-66-7255  
FAX: 49-(0)6196-66-7755  
E-mail: frank.goebel@samsung.com

#### Hungary Office

H2310, Szigetszentmiklos, Leshegy u.  
2-4, Hungary  
Tel: 36-24-551-148  
E-mail: jun21c.lee@samsung.com

### • Domestic Distributors

#### KORCHIP INC

#219-8 Gasan-dong, Gumchun-gu,  
Seoul, Korea  
Tel: +82-2-838-5588  
E-mail: hjh0064@korchip.com

#### CHUNG HAN

#16-96 Hangang-lo 3, Youngsan-Gu,  
Seoul, Korea  
Tel: +82-2-718-3322  
E-mail: bu1230choi@hanmail.net

#### SAMT

Daekyung Bldg., Daechi-Dong,  
Gangnam-Gu, Seoul, Korea  
Tel: +82-2-3458-9000  
E-mail: info@isamt.com

#### CHUNGMAC

#53-5 Wonhyolo3 Youngsan-gu,  
Seoul, Korea  
Tel: +82-2-716-6428~9  
E-mail: webmaster@anycam.co.kr

#### APEXINT

Room #905, C-dong Woorimlion's,  
Valley 371-28, Gasan-dong,  
Guemcheon-Gu, Seoul, Korea  
Tel: +82-2-2026-2610(2)  
E-mail: info@apexint.co.kr